

RECEIVED  
CENTRAL FAX CENTER

To: Technology Unit: 2816  
Facsimile Number: 703-872-9306

Total Pages Sent 5 JUL 16 2004

From: W. Daniel Swayze, Jr.  
Texas Instruments Incorporated  
Facsimile: 972-917-4418  
Phone: 972-917-5633

OFFICIAL

**FACSIMILE COVER SHEET**

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:

  
Tommie Chambers

July 16, 2004

Date

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input type="checkbox"/> AMENDMENT (# Pages)
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT (# Month)
<input type="checkbox"/> DECLARATION (# Pages)	<input type="checkbox"/> NOTICE OF APPEAL
<input type="checkbox"/> ASSIGNMENT (# Pages)	<input type="checkbox"/> APPEAL (# Pages)
<input type="checkbox"/> FORMAL DRAWINGS	<input type="checkbox"/> ISSUE FEE (# Pages)
<input type="checkbox"/> INFORMAL DRAWINGS	<input type="checkbox"/> PETITION TO REVIVE
<input type="checkbox"/> CONTINUATION APPN (# Pages)	<input checked="" type="checkbox"/> ELECTION
<input type="checkbox"/> DIVISIONAL APPN	
NAME OF INVENTOR(S): Barnett	
TITLE OF INVENTION: CIRCUIT AND METHOD TO COMPENSATE FOR RMR VARIATIONS AND FOR SHUNT RESISTANCE ACROSS RMR IN AN OPEN LOOP CURRENT BIAS ARCHITECTURE	
TI FILE NO.:	DEPOSIT ACCT. NO.:
TI-36061	20-0668
FAXED: 07/16/2004	
DUE: 07/29/2004	
ATTY/SECY: wds/tc	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/696,827 Filing Date: 10/30/2003	

This facsimile is intended only for the use of the address named and contains legally privileged and/or confidential information. If you are not the intended recipient of this telecopy, you are hereby notified that any dissemination, distribution, copying or use of this communication is strictly prohibited. Applicable privileges are not waived by virtue of the document having been transmitted by facsimile. Any misdirected facsimiles should be returned to the sender by mail at the address indicated on this cover sheet.

Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75265

RECEIVED  
CENTRAL FAX CENTER  
JUL 16 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Barnett Docket No: TI-36061  
Serial No: 10/696,827 Examiner: Wells, Kenneth B.  
Filed: 10/30/2003 Art Unit: 2816  
For: CIRCUIT AND METHOD TO COMPENSATE FOR RMR VARIATIONS  
AND FOR SHUNT RESISTANCE ACROSS RMR IN AN OPEN LOOP  
CURRENT BIAS ARCHITECTURE

RESPONSE

Assistant Commissioner For Patents  
Washington, DC 20231

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being  
transmitted by facsimile to the U.S. Patent and Trademark  
Office at 703-872-9306 on the date shown below:

  
Tommie Chambers Date

Dear Sir:

Responsive to the Office Action mailed June 29, 2004 in connection with the  
above identified application, Applicant(s) respectfully submit(s) the following remarks.

TI-36061 Page 1 of 4